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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

FEB 0 3 2004

In re Appln. of: V. JAIRAZBHOY, et al.

Appln. No.:

09/920,192

Filed:

August 1, 2001

For:

METHOD FOR MAKING AN

ELECTRONIC CIRCUIT

ASSEMBLY

Attorney Docket No: 10541-609

Examiner: Minh N. Trinh

Art Unit: 3729

REPLY UNDER 37 C.F.R. 1.111

In response to the Office Action mailed November 4, 2003 (Paper #8), Applicants respectfully submit this paper comprising the following parts:

- Amendments to the Specification, beginning on page 2; ١.
- 11. Listing of Claims, beginning on page 3;
- III. Remarks, beginning on page 6.

Applicants note that the Transmittal to which this paper is attached includes a Certificate of Facsimile Transmission under 37 C.F.R §1.8; and a fee statement calculating any fee(s) presently due in connection with the filing of this paper, along with an authorization to charge any fee deficiency to the deposit account of Applicant's assignee, Visteon Global Technologies, Inc., Deposit Account No. 06-1500.